

Electronic Patent Application Fee Transmittal

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|---|---|-----------------|---------------|-----------------------------|
| Application Number: | 10757788 | | | |
| Filing Date: | 15-Jan-2004 | | | |
| Title of Invention: | Method and apparatus for the automatic correction of faulty wires in a logic simulation hardware emulator / accelerator | | | |
| First Named Inventor/Applicant Name: | Thomas Michael Gooding | | | |
| Filer: | James R. Nock/Deb Peterson | | | |
| Attorney Docket Number: | ROC920030335US1 | | | |
| Filed as Large Entity | | | | |
| Utility Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Extension-of-Time: | | | | |
| Extension - 2 months with \$0 paid | 1252 | 1 | 450 | 450 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 450 |